

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	172861	reduc\$3 near3 power	US-PGPUB; USPAT	2007/03/16 12:21
2	BRS	L2	896200	processor microprocessor cpu mpu	US-PGPUB; USPAT	2007/03/16 12:21
3	BRS	L3	12161	1 same 2	US-PGPUB; USPAT	2007/03/16 12:22
4	BRS	L4	41960	datapath or (data adj path)	US-PGPUB; USPAT	2007/03/16 12:23
5	BRS	L5	517886	(allocat\$3 size width) near5 (chang\$3 alter\$3 reduc\$3)	US-PGPUB; USPAT	2007/03/16 12:26
6	BRS	L6	273746	(allocat\$3 size width) near5 (estimat\$3 determin\$3)	US-PGPUB; USPAT	2007/03/16 12:28
7	BRS	L7	534	4 same 5	US-PGPUB; USPAT	2007/03/16 12:28
8	BRS	L8	371	4 same 6	US-PGPUB; USPAT	2007/03/16 12:29
9	BRS	L9	28	3 and 7	US-PGPUB; USPAT	2007/03/16 13:19
10	BRS	L10	281	2 and 8	US-PGPUB; USPAT	2007/03/16 13:19
11	BRS	L11	6	3 and 8	US-PGPUB; USPAT	2007/03/16 13:28
12	BRS	L12	3742	713/300.ccls. 713/320.ccls.	US-PGPUB; USPAT	2007/03/16 13:28
13	BRS	L13	2049	713/322-324.ccls.	US-PGPUB; USPAT	2007/03/16 13:29
14	BRS	L14	5038	12 or 13	US-PGPUB; USPAT	2007/03/16 13:29
15	BRS	L15	3	10 and 14	US-PGPUB; USPAT	2007/03/16 13:38
16	BRS	L16	10908	1.clm.	US-PGPUB; USPAT	2007/03/16 13:38
17	BRS	L17	269946	2.clm.	US-PGPUB; USPAT	2007/03/16 13:39
18	BRS	L18	5648	4.clm.	US-PGPUB; USPAT	2007/03/16 13:39
19	BRS	L19	34027	5.clm.	US-PGPUB; USPAT	2007/03/16 13:39
20	BRS	L20	29142	6.clm.	US-PGPUB; USPAT	2007/03/16 13:40
21	BRS	L21	0	16 and 17 and 18 and 19 and 20	US-PGPUB; USPAT	2007/03/16 13:40
22	BRS	L22	24	16 and 17 and 18	US-PGPUB; USPAT	2007/03/16 13:41
23	BRS	L23	42052	allocat\$3.clm.	US-PGPUB; USPAT	2007/03/16 13:41

	Type	L #	Hits	Search Text	DBs	Time Stamp
24	BRS	L24	0	22 and 23	US-PGPUB; USPAT	2007/03/16 13:41